

ABSTRACT OF THE DISCLOSURE

A method of and an apparatus for performing efficient software emulation of a multi-
processor target computer by a host computer. The software technique permits multiple
processors to be emulated by a single processor. The use of software emulation permits the host
computer to execute both host programs and target programs. The software emulation is made
particularly efficient by utilizing the operation code combined with a separate four bit field to
directly address the corresponding host instructions.